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10/814,816	03/31/2004	Ligang Zhang	026-0041	5295
	7590 09/11/2007 BRIEN GRAHAM LLP	EXAMINER		
	I CAPITAL OF TEXAS HI	MATTHEWS, COLLEEN ANN		
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AUSTIN, TX 78731			2811	
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

		Application No.	Applicant(s)
A ***		10/814,816	ZHANG ET AL.
Οπιο	e Action Summary	Examiner	Art Unit
		Colleen A. Matthews	2811
The MAI Period for Reply	LING DATE of this communication	on appears on the cover sheet with	the correspondence address
WHICHEVER IS  - Extensions of time after SIX (6) MONT  - If NO period for rep  - Failure to reply with Any reply received	S LONGER, FROM THE MAILII may be available under the provisions of 37 ( HS from the mailing date of this communicat ly is specified above, the maximum statutory in the set or extended period for reply will, by	REPLY IS SET TO EXPIRE 3 MON NG DATE OF THIS COMMUNICA' CFR 1.136(a). In no event, however, may a reply ion. period will apply and will expire SIX (6) MONTHS y statute, cause the application to become ABANI e mailing date of this communication, even if time	TION.  be timely filed  from the mailing date of this communication.  DONED (35 U.S.C. § 133).
Status			
1)⊠ Responsi	ve to communication(s) filed on	<u>18 June 2007</u> .	
2a)⊠ This actio	n is <b>FINAL</b> . 2b)	This action is non-final.	
•	• •	illowance except for formal matters nder <i>Ex parte Quayle</i> , 1935 C.D. 1	
Disposition of Cla	ims	•	
4a) Of the 5) ☐ Claim(s) ☐ Claim(s) ☐ 7) ☑ Claim(s) ☐	1-5,7-46 and 48-63 is/are pendi above claim(s) is/are wi is/are allowed. 1-5,7,8,18,21-25,29-33,36-46,5, 9-17,19-20,26-28,34-35,48-51, are subject to restriction	thdrawn from consideration.  2,53,59 and 61-64 is/are rejected.  54-58, 60 is/are objected to.	
Application Papers	5	•	
•	fication is objected to by the Exa		
		☐ accepted or b)☐ objected to by	•
	• • • •	to the drawing(s) be held in abeyance.	
		correction is required if the drawing(s) in the Examiner. Note the attached O	
Priority under 35 U	J.S.C. § 119		•
a) All b) 1. Cer 2. Cer 3. Cor	Some * c) None of: rtified copies of the priority docurtified copies of the priority docu	uments have been received in Appl e priority documents have been rec	ication No
* See the att	ached detailed Office action for	a list of the certified copies not red	eived.
Attachment(s)		SUPERVIS	LYNNE GURLEY SORY PATENT EXAMINER
1) Notice of Referen	ces Cited (PTO-892)	4) Interview Sum	mary (PTO-413)
	erson's Patent Drawing Review (PTO-94 esure Statement(s) (PTO/SB/08) Date	· - /	ail Date mal Patent Application

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#### **DETAILED ACTION**

## Claim Rejections - 35 USC § 101

35 U.S.C. 101 reads as follows:

Whoever invents or discovers any new and useful process, machine, manufacture, or composition of matter, or any new and useful improvement thereof, may obtain a patent therefor, subject to the conditions and requirements of this title.

the claimed invention is directed to non-statutory subject matter.

Claims 40 is directed to a computer-readable medium, which may include "electronic", "wireless or other communications medium". As best understood from the written disclosure, this would include electromagnetic radiation waves, which are naturally occurring phenomenon and non-patentable subject matter.

### Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

<u>Claims 61-63 are rejected under 35 U.S.C. 112, second paragraph</u>, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Regarding claims 61-63, the word "means" is preceded by the word(s) "an apparatus comprising" in an attempt to use a "means" clause to recite a claim element as a means for performing a specified function. However, since no function is specified by the word(s) preceding "means," it is impossible to determine the equivalents of the

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element, as required by 35 U.S.C. 112, sixth paragraph. See *Ex parte Klumb*, 159 USPQ 694 (Bd. App. 1967).

### Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

<u>Claims 1, 2, 7, 8, 18, 21 - 25, 29, 30, 36 - 39, 41,42, 52, 53 and 59 are rejected</u> <u>under 35 U.S.C. 102(e)</u> as being anticipated by US Patent No. 6,847,282 to Gomez et al.

Regarding claims 1, 29 and 41, Gomez et al. teach an apparatus/method comprising: forming an electromagnetic shielding structure (516, 518, 1302) formed at least partially in one or more redistribution layers formed on an integrated circuit die, the electromagnetic shielding structure substantially surrounding a circuit element (510, 512); wherein the electromagnetic shielding structure has a top plate (518), a bottom plate (516) and sidewalls (Col. 8, lines 5 - 19).

Regarding claims 2, 30 and 42, Gomez et al. teach an apparatus/method, wherein the circuit element is formed partially in the redistribution layers (Col. 3, lines 28 -48).

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Regarding claim 7, Gomez et al. teach an apparatus, wherein the circuit element is substantially equidistant between the top and bottom plates (Fig. 5).

Regarding claim 8, Gomez et al. teach an apparatus, wherein the circuit element is positioned between top and bottom plates. With respect to the requirement that the positioning be based on resistivities of the top and bottom plates, this is considered a product by process limitation and is not given any patentable weight.

Regarding claims 18 and 52, Gomez et al. teach an apparatus/method, wherein the redistribution layers include at least one redistribution metal layer and at least one redistribution dielectric layer (Col. 3, lines 28 - 38).

Regarding claims 21 and 59, Gomez et al. teach an apparatus, wherein the circuit element comprises an inductor structure.

Regarding claim 22, Gomez et al. teach an apparatus, wherein the inductor structure comprises a parallel-connected pair of inductor loops.

Regarding claim 23, Gomez et al. teach an apparatus, wherein the current flow through the pair of inductor loops is substantially balanced.

Regarding claim 24, Gomez et al. teach an apparatus, wherein the pair of inductor loops are formed in a planar configuration.

Regarding claim 25, Gomez et al. teach an apparatus, wherein the pair of inductor loops are formed in a vertical configuration.

Regarding claim 36, Gomez et al. teach a method further comprising:

Providing the circuit element spaced from the electrically conductive enclosure sufficiently spaced to limit the capability of the electrically conductive enclosure from

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generating an electromagnetic that counteracts an electromagnetic field generated by the circuit element (this is the purpose of the shielding).

Regarding claim 37, Gomez et al. teach a method, further comprising:

effectively shielding with the electrically conductive enclosure the circuit element

from electromagnetic signals of particular frequencies generated by external elements.

Regarding claim 38, Gomez et al. teach a method, further comprising: effectively preventing electromagnetic signals of particular frequencies generated by the circuit element from effecting external elements using the electrically conductive enclosure.

Regarding claim 39, Gomez et al. teach a method, wherein the circuit element is an inductor structure.

Regarding claim 53, Gomez et al. teach a method, wherein the redistribution metal layer includes copper (Col. 3, lines 28 - 35).

Claim 64 is rejected under 35 U.S.C. 102(e) as being anticipated by US Patent No. 2004/0049745 to Rahman et al.

Regarding claim 64, Rahman et al. discloses a computer-readable medium including a integrated circuit (see for example page 3, claim 8) where the computer-readable meidume encoding includes at least one of disk, tape, and semiconductor medium (disk, tape & semiconductor medium all disclosed in page 3, claim 9).

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# Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

<u>Claims 3 - 5, 31 - 33 and 43 - 46 are rejected under 35 U.S.C. 103(a)</u> as being unpatentable over Gomez et al. in view of US Patent No. 6,790,759 to Wang et al.

Regarding claims 3, 5, 31, 33, 43, 44 and 46, Gomez et al. teach an apparatus/method, wherein the redistribution layers are formed above a non-conductive layer. Gomez et al. do not teach that the layer is a passivation layer.

Wang et al. teach a passivation layer (57) disposed beneath redistribution layers and covering a circuit element. It would have been obvious to one of ordinary skill in the art to use a passivating material, since it is desirable to protect the underlying circuit element.

Regarding claims 4, 32 and 45, Gomez et al. do not explicitly teach that the redistribution layers are formed above integrated circuit pads.

Wang et al teach redistribution layers (59) above integrated circuit pads (61; Col. 5, lines 55 - 67) It would have been obvious to one of ordinary skill in the art to include integrated circuit pads, since it is desirable to provide reliable connection to the underlying circuit elements.

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### Response to Arguments

Applicant's arguments filed 06/18/2007 have been fully considered but they are not persuasive.

Applicant argues (Remarks page 10 paragraph 2) that the Office has failed to provide a basis for the conclusion of unpatentablity of claim 40 under 35 U.S.C. 101. The rejection is maintained in accordance with MPEP 2106.01.

Applicant argues (Remarks page 11 – 14) that Gomez fails to teach or suggest one or more redistribution layers. Examiner disagrees. Gomez discloses the use of redistribution layers, for example in col 3 lines 29-38 and 43-48 Gomez discloses use of traces and conductive routing made of Copper and the use of transmission lines.

Although Gomez does not specifically use the term "redistribution layer" the layers that are described in Gomez have the same function as a redistribution layer.

### Allowable Subject Matter

Claims 9 -17,19, 20, 26 - 28, 34, 35, 48 - 51, 54 - 58 and 60 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

#### Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, THIS ACTION IS MADE FINAL. See MPEP

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§ 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Colleen A. Matthews whose telephone number is 571-272-1667. The examiner can normally be reached on Monday - Friday 8AM-4:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne Gurley can be reached on 571-272-1670. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

CAM 09/03/2007

LYNNE GURLEY
SUPERVISORY PATENT EXAMINER

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